

Amendment to the Specification:

Rewrite paragraph number [0033]¹ to read as follows:

[0033] The receiver 12 includes a voltage controlled crystal oscillator (VCXO) 46 which produces on a line 48 a sampling clock signal for the ADC in the unit 32, synchronized to the 2.208 MHz sampling frequency of the transmitter 10 by a control loop which includes a phase comparator 50 and digital and analog control loop filters represented by a unit 52. The FEQ and decoder unit ~~36~~ 40 supplies the phase information of the received pilot tone via a line 54 to the phase comparator 50, and a stored reference phase is also supplied to the phase comparator 50 from a store 56. The phase comparator 50 produces at its output a digital phase error control signal which is filtered by digital and analog filters in the unit 52 to produce an analog control voltage; this is used to control the VCXO 46 to maintain frequency synchronization.

On page 7,² in the first line, rewrite the heading:

DETAILED DESCRIPTION OF THE INVENTION

¹ Which is paragraph number [0030] according to the substitute specification.

² Of the substitute specification.